



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Raymond J. Beffa

Serial No.: 10/600,148

Filed: June 19, 2003

For: METHOD IN AN INTEGRATED CIRCUIT (IC) MANUFACTURING PROCESS FOR IDENTIFYING AND REDIRECTING IC'S MIS-PROCESSED DURING THEIR MANUFACTURE

Confirmation No.: 6251

Examiner: P. Assouad

Group Art Unit: 2857

Attorney Docket No.: 2269-3040.5US
(96-0970.05/US)

Notice of Allowance Mailed:

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Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Amendments to the Title appear on page 3 of this paper.

Amendments to the Specification begin on page 4 of this paper.

Amendments to the Abstract appear on page 10 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 11 of this paper.

Remarks begin on page 18 of this paper.

IN THE TITLE:

The title has been amended herein. Pursuant to 37 C.F.R. §§ 1.121 and 1.125 (as amended to date), please enter the title as amended.

**METHOD IN AN INTEGRATED CIRCUIT (IC) MANUFACTURING
PROCESS FOR IDENTIFYING AND REDIRECTING IC'S ICs
MIS-PROCESSED DURING THEIR MANUFACTURE**